

DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION

Attorney's Docket No.: 30003829 US

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name;

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

Block Error Rate Measurements

the specification of which

X is attached hereto;

 was filed on as Application Serial No. , and was amended on (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, §1.56 (a).

I hereby claim foreign priority benefits under Title 35, United States Code §119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate filed by me on the same subject matter having a filing date before that of the application(s) of which priority is claimed:

Prior Foreign Application(s)			Priority Claimed
<u>0013746.3</u>	<u>GB</u>	<u>07 June 2000</u>	<u>X</u> Yes <u> </u> No
Number	Country	Day/Mo/Yr Filed	
<u>00311334.7</u>	<u>European Patent Office</u>	<u>18 December 2000</u>	<u>X</u> Yes <u> </u> No
Number	Country	Day/Mo/Yr Filed	
<u> </u>	<u> </u>	<u> </u>	<u> </u> Yes <u> </u> No
Number	Country	Day/Mo/Yr Filed	

I hereby claim the benefit under Title 35, United States Code §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United

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States Code, §112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, §1.56, which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial #) (Filing Date) (Status) (patented, pending, abandoned)

POWER OF ATTORNEY: As a named Inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

Richard P. Berg, Reg.No. 28,145

SEND CORRESPONDENCE TO:
Richard P. Berg, c/o Ladas & Parry
5670 Wilshire Boulevard, 21st floor
Los Angeles, California 90036

DIRECT TELEPHONE CALLS TO:
Richard P. Berg, (323)-934-2300

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of sole or first inventor: Peter James Sutherland Goldsack

Inventor's Signature: *Peter J. Goldsack*

Dated: 25 April '01

Residence: Edinburgh, Scotland

Citizenship: British

Post Office Address: 47/10 West Bryson Road, Edinburgh EH11 1BQ, Scotland

Full name of 2nd joint inventor, if any: Gerard Fitzpatrick

Inventor's Signature: *Gerard Fitzpatrick*

Dated: 25 April '01

Residence: Dalkeith, Midlothian, Scotland

Citizenship: British

Post Office Address: 21 Mitchell Street, Dalkeith, Midlothian, EH22 1JQ, Scotland

Full name of 3rd joint inventor, if any: Abijah M. Perkins

Inventor's Signature:

Dated:

Residence: Liberty Lake, Washington, USA

Citizenship: US

Post Office Address: 1324 N. Liberty Lake Road #263, Liberty Lake, WA 99019, USA

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Block Error Rate Measurements

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X is attached hereto;

__ was filed on _____ as Application Serial No. _____, and was amended on _____ (if applicable).

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of Federal Regulations, §1.56, which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

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Full name of sole or first inventor: Peter James Sutherland Goldsack
Inventor's Signature:

Dated:

Residence: Edinburgh, Scotland
Citizenship: British
Post Office Address: 47/10 West Bryson Road, Edinburgh EH11 1BQ, Scotland

Full name of 2nd joint inventor, if any: Gerard Fitzpatrick
Inventor's Signature:

Dated:

Residence: Dalkeith, Midlothian, Scotland
Citizenship: British
Post Office Address: 21 Mitchell Street, Dalkeith, Midlothian, EH22 1JQ, Scotland

Full name of 3rd joint inventor, if any: Abijah M. Perkins
Inventor's Signature:

Dated: 1 - May - 2001

Residence: Liberty Lake, Washington, USA
Citizenship: US
Post Office Address: 1324 N. Liberty Lake Road #263, Liberty Lake, WA 99019, USA